

# GERDA: a novel Ge-detector operation technology for neutrinoless $\beta\beta$ decay searches

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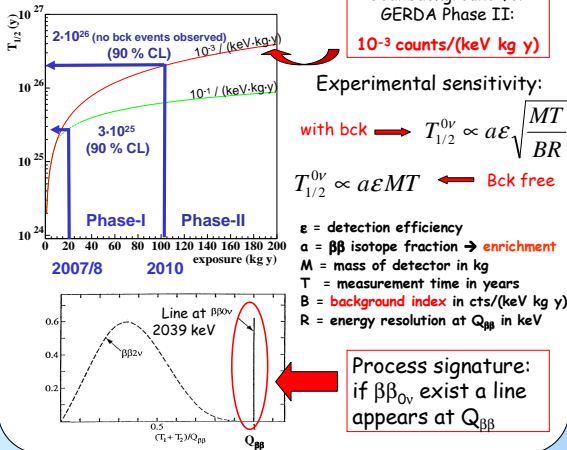
for the GERDA collaboration

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## The Gerda $0\nu 2\beta$ experiment

GERDA is a new experiment for the search of  $^{76}\text{Ge}$  neutrinoless  $\beta\beta$  decay at the Gran Sasso Laboratory (LNGS). Ge detectors made out of isotopically enriched (~86%) material inside a cryogenic fluid shield.

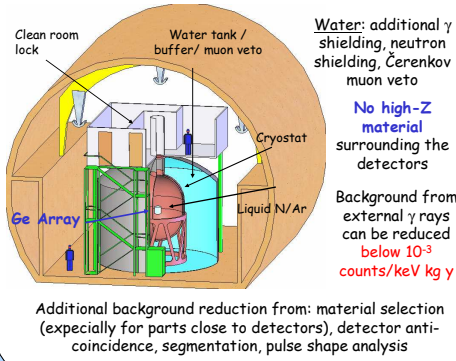


## Experimental procedure

### Phased approach:

Phase I: existing detectors of former HM & IGEX experiments, properly refurbished to be operated naked in LN  
 Phase II: new detectors

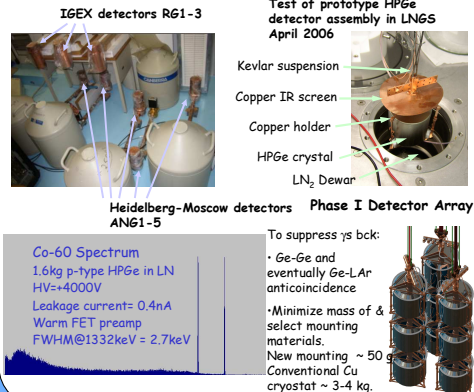
GERDA design: graded shielding. Inner liquid N/Ar shielding + external water buffer



## Phase I detectors

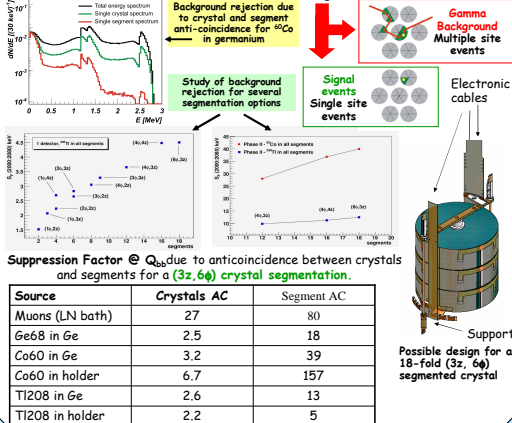
Eight enriched detectors from the former Heidelberg-Moscow and IGEX experiments have been underground for more than ten years  $\Rightarrow$  Internal cosmogenic background reduced. Total mass 17.9 kg. A procedure for the removal from their actual cryostat, re-contacting and mounting inside LN bath while keeping their radiopurity quality has been developed.

Detectors	ANG1	ANG2	ANG3	ANG4	ANG5	R61	R62	R63
FWHM [keV]	2.54	2.29	2.93	2.47	2.59	2.21	2.31	2.26
Mass [kg]	0.968	2.906	2.446	2.400	2.781	2.150	2.194	2.121



## Segmented detectors for Phase II

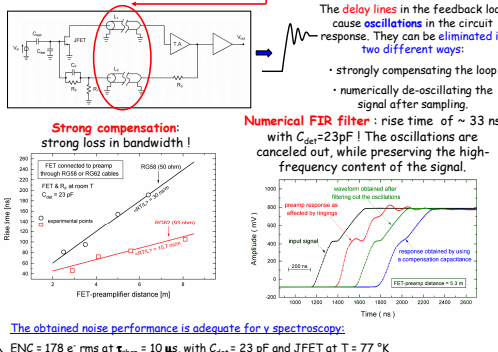
The segmentation of the diode read-out is a powerful background rejection tool, to be used in the Phase II of GERDA;  $\gamma$ s (bck) events are multi-site, while  $\beta\beta$  are single-site



## Front-end electronics: 1st solution

Cold BF862 JFET (inside LN bath) + warm hybrid preamplifier (outside LN bath)

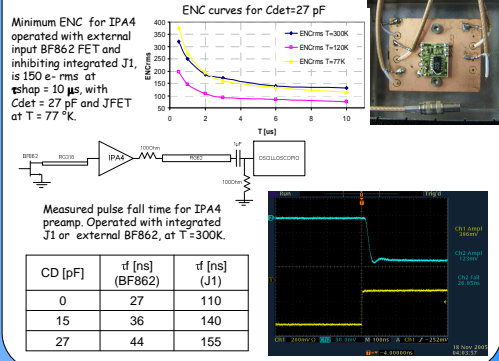
The hybrid preamplifier, typically realized with BJTs, cannot work at cryogenic temperature, owing to carrier freeze-out phenomena. It has been connected to the front-end JFET placed in LN bath, by 2x6 m long coaxial cables.



## Front-end electronics: 2nd solution

Cold monolithic JFET preamplifier (inside LN bath)

IP4A monolithic JFET preamplifier, developed for gamma spectroscopy in cryogenic environment has been tested at LN temperature and showed superior noise performances, and, once operated with an external input FET as first stage, satisfying time performances to implement PSA.



## Front-end electronics: 3rd solution

ASIC CMOS preamplifier in LN bath

First rule to reduce significantly background is to minimize mass of each component close to crystals  $\rightarrow$  integrated front end.

Two ASIC CMOS circuits under development and test

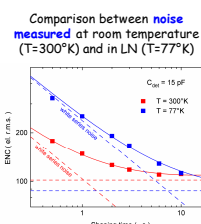
0.8  $\mu\text{m}$  5V CMOS single or differential ended preamp with:

- external input stage (JFET) or
- integrated input FET

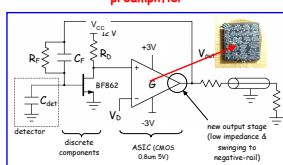
external feedback components

First measurement results at  $T=77\text{K}$   
 $C_f = 0.15\text{pF}$ ,  $R_f = 16\Omega$ ,  $C_{det} = 15\text{pF}$

Negative output voltage swing: ~ 2.4 V  
 Energy sensitivity ( $C_f = 0.15 \text{ pF}$ ): ~ 185 mV/MeV after 50  $\Omega$  termination  
 Input dynamic range: ~ 6.5 MeV  
 Rise time: ~ 15 ns driving 10m cable  
 Minimum ENC ( $C_{det} = 15\text{pF}$ ): 112 e<sup>-</sup> at  $\tau_{shp}=10\mu\text{s}$   
 Total power consumption: ~ 25 mW



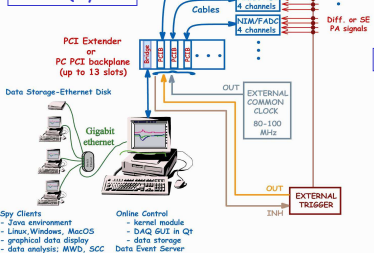
Tested circuit structure: external BF862 JFET + 0.8  $\mu\text{m}$  5V CMOS single-ended preamplifier



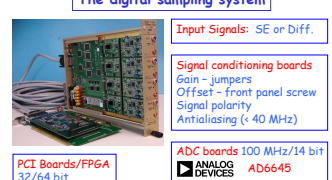
## Digital sampling data acquisition system

- PSA to reduce the background produced by multi-site events
- digital filters can improve detector response when signals are affected by microphonics and/or high ripple
- detector test and characterization
- building of pulse shape databases for the PSA algorithms

### General layout of the DAQ system



### The digital sampling system



### Results obtained with a low-noise planar Ge detector

