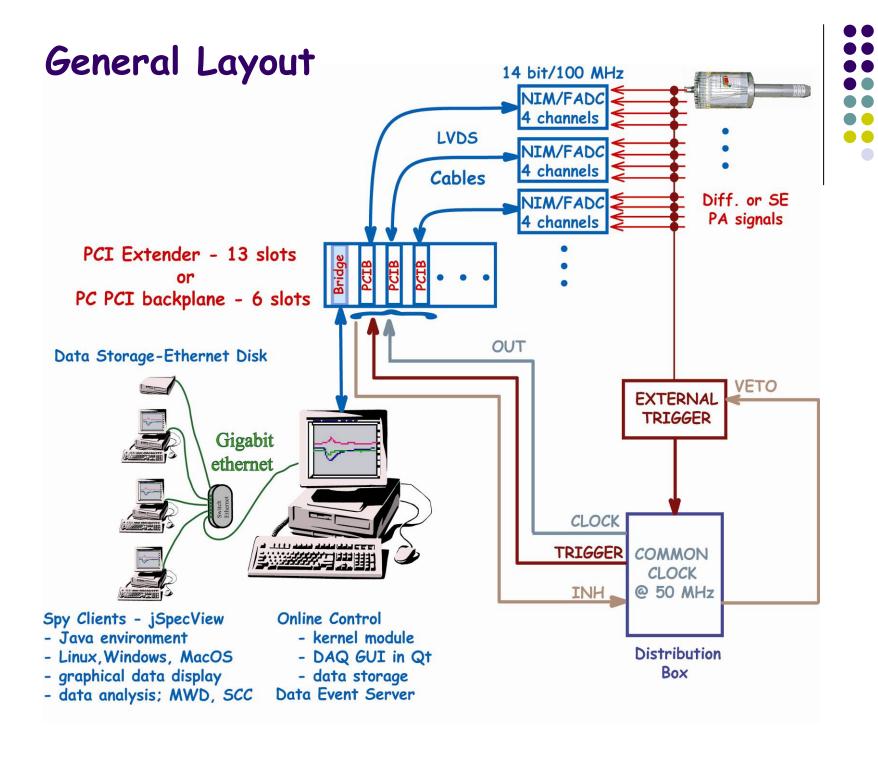


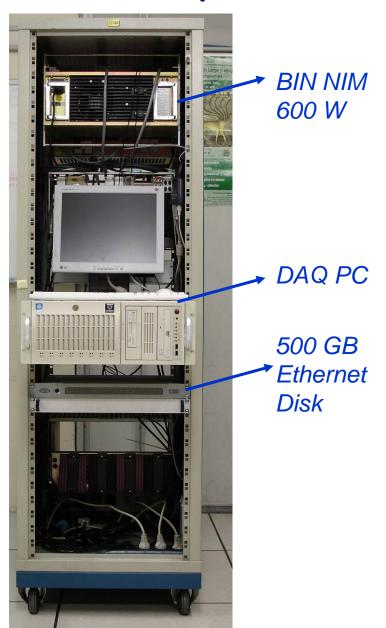
# The PCI-NIM Based DAQ System

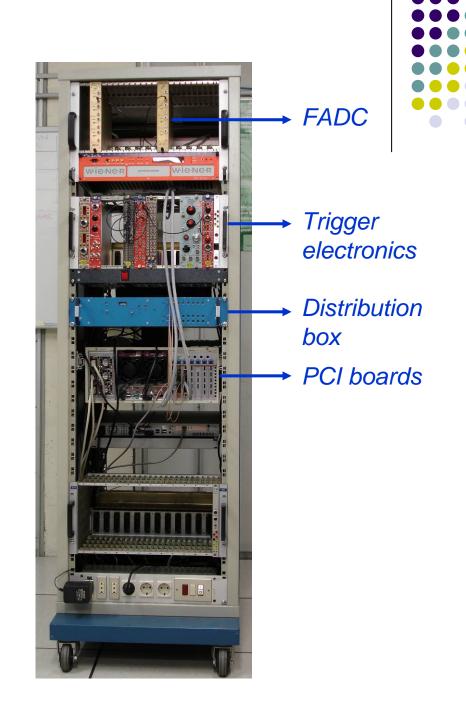
Călin A. Ur INFN - Padova





# General Layout





# The Digital Sampling Modules

Collaboration Padova – Milano (2005 – 2006)

LVDS Transmitters

FPGA Data

**Transmission** 

+ 12 V 1.35 A - 12 V 1.35 A + 6 V 1.40 A - 6 V 1.40 A



- modularity for future upgrades
- stability against manipulation
- good electrical contact

**Motherboard** 

<u>Daughterboards</u>

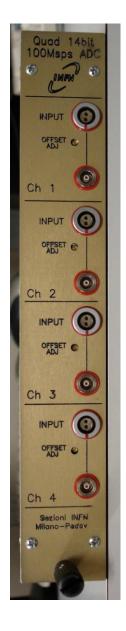
Signal Conditioning Boards

FADC boards



14 bit 100 MHz

## The Signal Conditioning Board



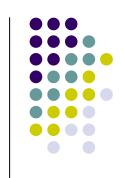
- input signals
  - differential -1 V to +1 V
  - single ended 0 V to +1 V
  - positive or negative
- offset
  - screw adjusted continuously
- gain
  - set by jumpers
- analog frequency
  - up to 40 MHz

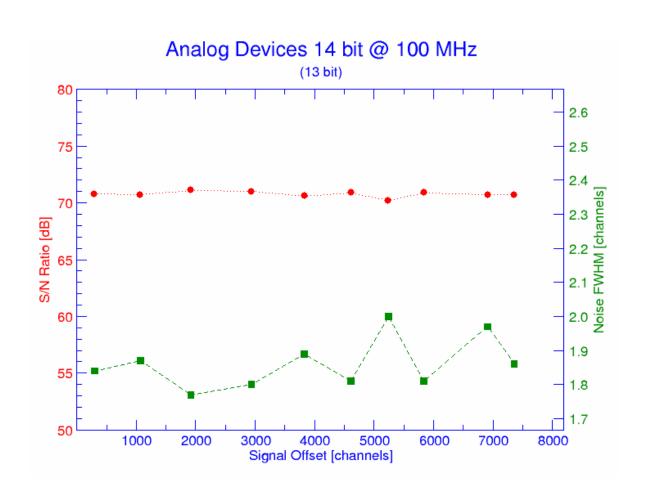




## Effective Number of Bits

Noise measurement without input signal

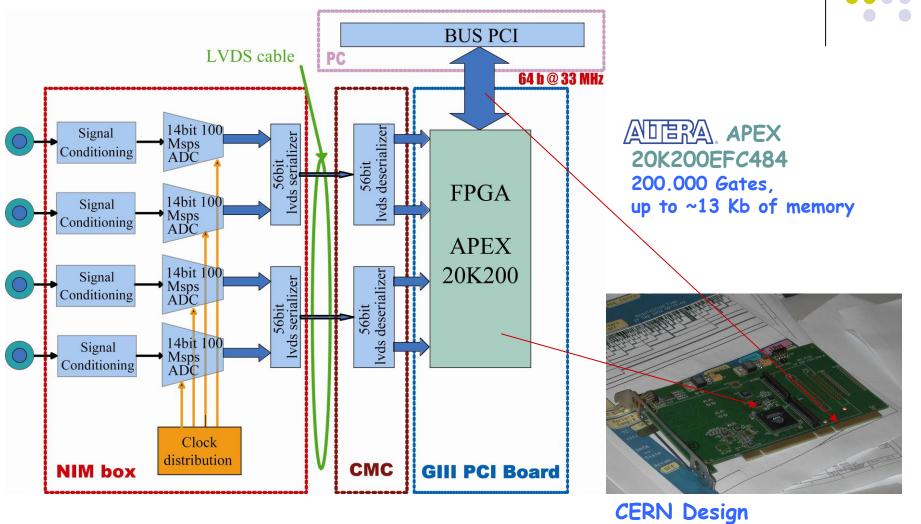




**ENOB = 11.5** 

## Data Readout





# Main Features of the DAQ System

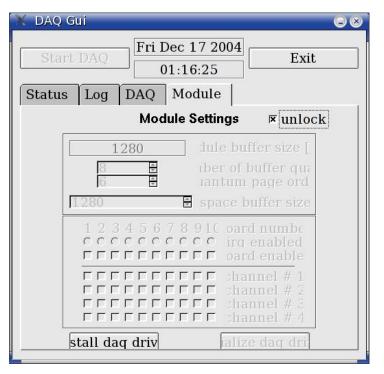


	MD2S Padova
Channels	4
FADC bits	14 (13)
FADC rate (MHz)	100
FPGA k-gates/ch	100
Internal trigger	no
Trace length (samples)	max 2048
Control & i/f	NIM/PCI
Data xfr	PCI
Max output rate (MB/s)	6

## DAQ Control

The acquisition software is implemented for Linux O.S.

- >Setup the DAQ configuration
- > Manage the communication between PC and PCI cards
- ➤Interrupt-based data transfer (6 MB/s)
- >Enable/disable the trigger between the data saving



#### www.kernel module linux 2.4

- handles the interrupts from PCI cards
- transfers the data from PCI cards
- minimal data manipulation
- pipes the data into a character device

### 🖫 user program

- takes the data from the character device
- writes the data on HD

## • graphical user interface

- developed in Qt 3
- controls module loading and data manipulation
- start/stop of the acquisition



## Data Format

#### Header

```
Data Label
?MSDAQ01
           Number of Channels
?CHN0003
           Number of Sampling Points
?P002048
?SPR0016
           Sample Precision in Bits
           Number of ADC Conversion Bits
?LBT0013
?LTR0016
           Trailer Length in Bytes
?CH10002
           Channel #1 Enable Pattern 000000010
?CH20000
           Channel #2 Enable Pattern 0000000000
?CH30001
           Channel #3 Enable Pattern 000000001
           Channel #4 Enable Pattern 000000010
?CH40002
?MHZ0100
           Sampling frequency
?RUN0007
           Run No.
?ORIGDAT
           Original data
?U000029
           User Comment Length (in bytes)
"Data Comment with User Comment Text"
"new lines"
"...." up to 512 characters
           End of Header
?ENDHEAD
```



## Data Format



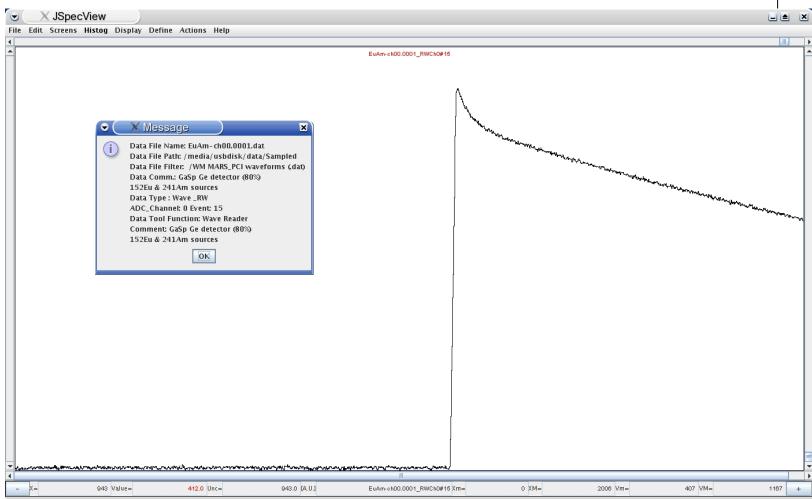
#### Data Block



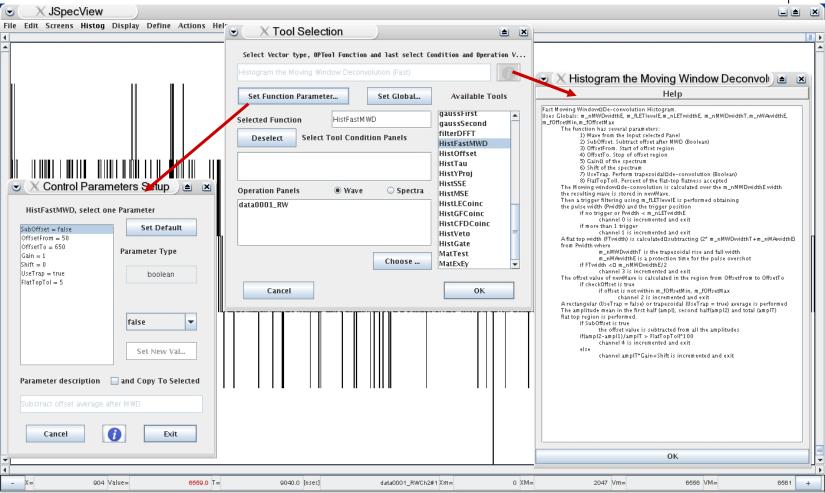
## Graphical viewer and analysis program

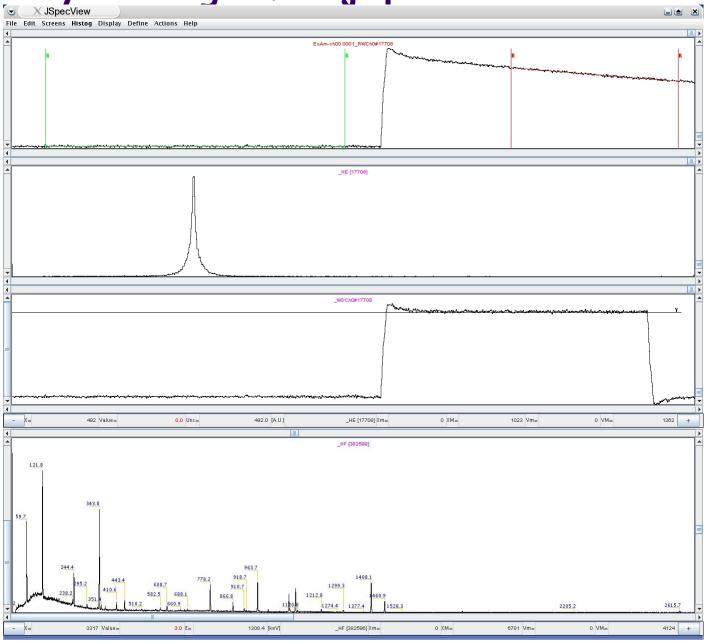
- > platform independent (Java)
- > manipulation of digital waves digital filters
- > building and saving of histograms
- > setting gates and building histograms with conditions
- > standard spectra operations (calibration, fit, integral, ...)
- > oscilloscope function
- > generating and viewing of 2-D matrices





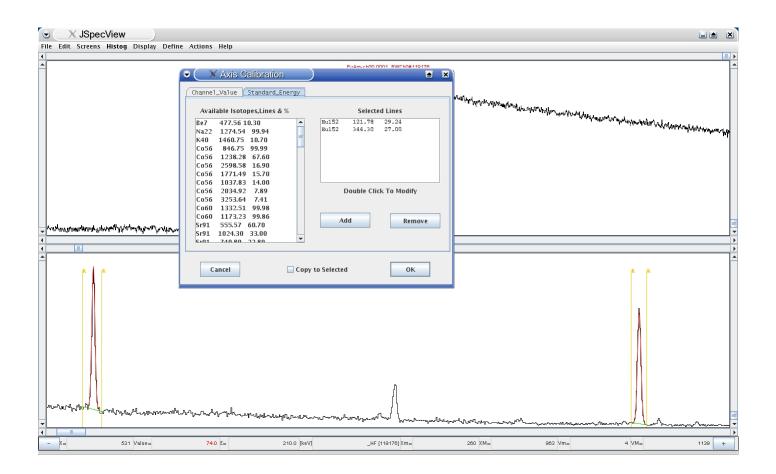






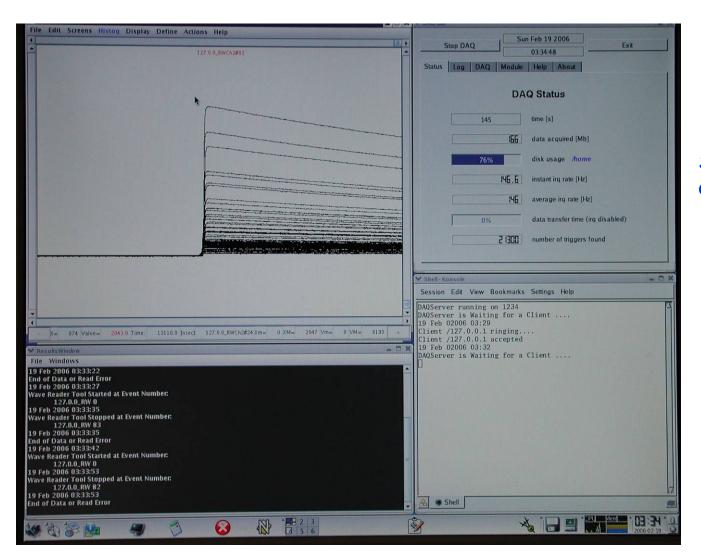






# Online Monitoring - jSpecView Server





Server - Client over IP

## Status & Perspectives



- > the PCI NIM DAQ system is operational
- > data analysis and online monitoring program working
- > transfer to LNGS
  - > 2 modules end of July
  - > + 2 modules end of September
  - > + 2 modules end of the year