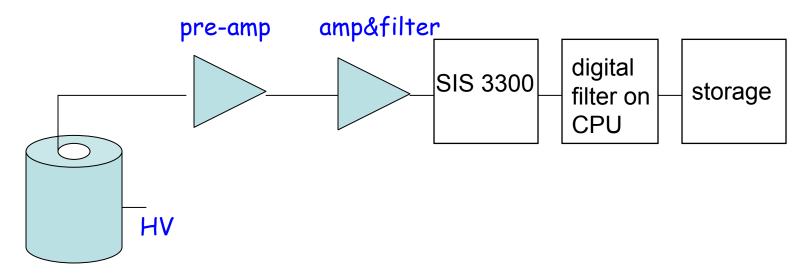
## Readout Electronics

B.Schwingenheuer, MPI HD

## Electronics a la T.Kihm et al., NIM B498 (2003) 334



pre-amp: charge sensitive amp, directly at Ge diode? without package? self-made? on Kapton cable?

SIS 3300: 8-channel VME module, 12 bit ADC, 100 MHz sampling, self-triggering (difference of 2 time slices with gap) 2 memory banks of 128k samples, readout ~ 7 MB/sec

digital filter: on VME CPU, for pulse shape analysis (rise time), integration, pole/zero cancellation advantage: time stability (0.007% in 3 weeks), better resolution?, pile-up handling?